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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/550,346	09/23/2005	Kong Lim Toh	DE 030087	1325

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NXP, B.V.  
NXP INTELLECTUAL PROPERTY DEPARTMENT  
M/S41-SJ  
1109 MCKAY DRIVE  
SAN JOSE, CA 95131

EXAMINER
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LEE, BENNY T

ART UNIT	PAPER NUMBER
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2817

NOTIFICATION DATE	DELIVERY MODE
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03/17/2008

ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

<b>Office Action Summary</b>	<b>Application No.</b> 10/550,346	<b>Applicant(s)</b> TOH, KONG LIM	
	<b>Examiner</b> Benny Lee	<b>Art Unit</b> 2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 26 November 2007 & 4 February 2008.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1 and 4-9 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 and 4-9 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 September 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)          | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

35 U.S.C. 112, first paragraph, requires the specification to be written in "full, clear, concise, and exact terms." The specification is replete with terms which are not clear, concise and exact. The specification should be revised carefully in order to comply with 35 U.S.C. 112, first paragraph. Examples of some unclear, inexact or verbose terms used in the specification are: At all occurrences through out the specification, note that the term "analogue" should be appropriately spelled as --analog--.

The disclosure is objected to because of the following informalities: Note that subheadings should be provided to delineate the different portions of the specification (e.g. --Summary of the Invention--; --Brief Description of the Drawings--; etc) for clarity of description. Appropriate correction is required.

The disclosure is objected to because of the following informalities: Note that the following reference labels appearing in the indicated drawing figure needs a corresponding specification description relative to that drawing figure: Fig. 2 ( $I^2C$ ); fig. 4 (48, 50, 52, PESW). Appropriate correction is required.

The drawings are objected to because of the following: In figs. 3, 5, should "5V\_SW" be rewritten as --5V\_V<sub>SW</sub>-- for an appropriate characterization?; In Fig. 3, should "V<sub>SW</sub>" properly be --V<sub>SW</sub>-- for an appropriate characterization? Contrary to applicant's assertion, it should be noted that replacement drawings do not appear to have been filed. Accordingly, the above drawing objections have not been addressed and the objections thereto have been sustained.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet,

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even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as “amended.” If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either “Replacement Sheet” or “New Sheet” pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 CFR 1.75(d)(1) and MPEP § 608.01(o). Correction of the following is required: The specification still needs to provide a corresponding description that the “second switch” is “an integrated circuit” such as recited in claim 5.

Claim 7 is rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential structural cooperative relationships of elements, such omission amounting to a gap between the necessary structural connections. See MPEP § 2172.01. The omitted structural cooperative relationships are: In claim 7, an operative connection between the first driver circuit and the second driver circuit with respect to the “driver terminal” as recited in claim 1.

Claims 1, 4-9 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, note that it is unclear whether the recitation of high and low “insertion loss” between the common port and the corresponding branch port can properly apply “in each state” as recited. That is to say, the recited high and low “insertion loss” between the common port and the corresponding branch port appears to apply to only one switch state rather than “each” switch state. Clarification is needed.

The following claims have been found objectionable for reasons set forth below:

In claim 1, line 3, note that --first switch-- should follow “each” for an appropriate characterization; lines 5, 6, note that --corresponding-- should precede each occurrence of “first and second” for an appropriate characterization; line 7, note that --a respective-- should precede “one of said first switches” for an appropriate characterization.

In claim 8, note that “I2C” should be rewritten as --I<sup>2</sup>C-- for an appropriate characterization.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 4, 5, 6, 7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Heckaman et al (of record) in view of Even-or.

Heckaman et al (Fig. 6) discloses a switch circuit device comprising: two input terminals (i.e. RF IN 28, RF IN 30) and an output terminal (i.e. RF OUT 35); first switches (i.e. SPST switch modules 20, 22) having first and second ports (i.e. one port is connected to the corresponding RF IN terminal; another port is connected to corresponding transmission lines 36,

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38); and a second switch (i.e. SPDT module 24) having branch ports connected to corresponding SPST modules (20, 22) via corresponding transmission lines (36, 38). As known to those of ordinary skill in the art, an SPST (i.e. single pole-single throw) switch functions to provide either a high insertion loss (i.e. open) state or a low insertion loss (i.e. closed) state depending on the bias voltage (i.e. 5V/0V) applied to the transistors of the corresponding SPST switch. In a similar manner, an SPDT (i.e. single pole-double throw) switch functions to provide either a low insertion loss (i.e. closed) state to one of the branches while providing a high insertion loss (i.e. open) state to the other one of the two branches or vice versa depending on the complementary bias voltage (i.e. 5V/0V or 0V/5V) applied to the corresponding branch. Regarding claim 4, it should be noted that each one of SPST switch module comprises of a plurality of “discrete electronic parts” (i.e. a plurality of transistors”. Regarding claim 5, note that Fig. 13, which is a physical realization of the switch in the fig. 6 embodiment, discloses that the switches are disposed in an “integrated circuit” configuration upon a substrate. With regard to the operation of the bias voltage being applied to the corresponding switches, note that the description at column 5, line 64 to column 6, line 2 and column 6, lines 11-21 describe how only two bias voltages or “drivers” are needed to provided the complementary bias control voltages to selectively switch the SPST & SPDT switch modules.

Note that Heckaman et al (Fig. 6) discloses the claimed invention except that the “first switches” are implemented by transistors and not by series connected PIN (i.e. switching) diodes having a driver control connected to the junction between the diodes.

However, as disclosed in an alternative realization of the switches, Heckaman et al (Fig. 3) suggests that such switches can alternatively be realized by switching PIN diodes.

Moreover, Even-or (e.g. Fig. 1A) discloses a diode switch configuration having two series connected diodes (i.e. D1, D2) connected between an input (i.e.  $RF_{IN}$ ) and an output (i.e.  $RF_{OUT}$ ) to thereby function in an SPST mode in response to a control signal (i.e. ON/OFF) applied to the junction between the diodes (D1, D2) as provided by a control driver circuit (e.g. 12, 14, 18, 20, 22).

Accordingly, it would have been obvious in view of the references, taken as a whole, to have modified the SPST switches in Fig. 6 in Heckaman et al with the series connected PIN diodes as taught by Even-or. Such a modification would have been considered an obvious substitution of art recognized components usable in an SPST switch, especially since Heckaman et al recognizes that PIN diodes are an equivalent type of switching element to transistors and as such would have performed the equivalent function as the transistors within the context of the disclosed SPST switch, thereby suggesting the obviousness of alternatively using series connected diode switches in place of transistor switches.

Claims 8, 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over the above rejection of claims 6 & 1, respectively and further in view of Atokawa et al (of record).

As previously described, the above obviousness combination discloses the claimed SPST/SPDT switch combination, but does not disclose the use of such a switch combination in conjunction with a transceiver circuit.

Atokawa (fig. 1) discloses a transceiver circuit (i.e. transmit/receive filter 1) having two input terminals (i.e. antennas 8, 9), a tuner circuit (i.e. receive filter 3); and a switch circuit (i.e. SPDT switch (4) operatively connected to switches (6, 7) which equivalently function as SPST switches) selectively connecting the antennas (8, 9) to the tuner input (i.e. ant2).

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Accordingly, it would have been obvious in view of the references, taken as a whole, to have realized the switch circuit (i.e. SPDT switch (4) in conjunction with SPST switches 6, 7) in Atokawa et al by the electrically equivalent switch in the above obviousness combination. Such a modification would have been considered an obvious substitution of art recognized equivalent switches, especially since the switch in Atokawa et al has the same electrical configuration as the switch combination set forth above, thereby suggesting the compatability and thus the obviousness of such a modification. Moreover, as disclosed in Atokawa et al, each switch is electrically connected to a control circuit for controlling the switching state of the transceiver.

Applicant's arguments with respect to claims 1-9 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication should be directed to Benny Lee at telephone number 571 272 1764.

**/BENNY LEE/  
PRIMARY EXAMINER  
ART UNIT 2817**

B. Lee